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J1036 U.S. PTO  
09/940010



08/27/01

714	724	Subclass
Class	ISSUE CLASSIFICATION	

SV

PATENT NUMBER
<b>6546512</b>
6546512

U.S. UTILITY Patent Application

O.I.P.E. SCANNED <i>KC CTH</i> O.A. <i>Am</i>	PATENT DATE APR 08 2003
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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/940010	D	714	724	<del>2133</del> 2133	Chase

APPLICANTS  
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TITLE  
Semiconductor integrated circuit having compression circuitry for compressing test data, and the test system and method for utilizing the semiconductor integrated circuit

PTO-2040  
12/99

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ISSUING CLASSIFICATION							
ORIGINAL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
714	724	324	765				
INTERNATIONAL CLASSIFICATION							
G 01 R 31/28							

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1-1503 Formal Drawings 5 sheets 8-27-01

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg. 5	Figs. Drwg. 5	Print Fig. 2	Total Claims 14	Print Claim for O.G. 7
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	Shelly A Chase 9-5-02 (Assistant Examiner) (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>  9-10-02	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____	ALBERT DECADY SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2100 A Decady 9/9/02 (Primary Examiner) (Date)			<b>ISSUE FEE</b> (w) Amount Due \$1280.00 Date Paid 12-3-02	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	V. J. 9-15-02 (Legal Instruments Examiner) (Date)			<b>ISSUE BATCH NUMBER</b>	

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Formal Drawings 5 sheets 351

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